<u>RESUME</u>

D.Srinu,
H.NO.4-1-153/6,
Zaheerpura,
Opp: Timber Depot Area,
Khammam.-Urban-Dist-A.P

Contact Num: **9948834790**
Email ID: **Srinu_440@yahoo.co.in**
srinudhonvan@gmail.com

CAREER OBJECTIVE

To develop my career where I will be a valuable team members contributing quality ideas and work for an organization where there is an ample scope for individuals as well as organization growth.

EDUCATIONAL QUALIFICATIONS:

Course	University	Institute	Year	Percentage/CGPA
Master of Technology (VLSI System Design)	JNTU Hyderabad	Aurora Engineering College, Bhongir	2007-2009	68.96%
Bachelor of Technology (Electronics&Commnication Engineering)	JNTU Hyderabad	Vaagdevi Institute of Science and Technology, Warangal (Dist)	2000- 2004	56.97%
12th Standard (M.P.C)	Board of Intermediate Education (Intermediate)	Baby Moon Junior College, Khammam.	1998-2000	73.3 %
10th Standard	Board of Secondary Education (SSC)	Baby Moon High School, Khammam.	1998	**77.8 %**

B.Tech Project:

Tittle   :   Speech compression and De compression using ADPCM

Team member   :   4 Members

Place   :   Vaagdevi College of Engg.

M.Tech Project: :   Automatic interface synthesis based on classification of interface Protocols of IP'S

SOFT SKILLS

- ❖ C programming,
- ❖ VHDL, Verilog.
- ❖ MultiSim

EXPERIENCE:

- ❖ Worked in Anurag Engg. College in Nalgonda as lecturer during 2004-2007.
- ❖ Worked in Puli Pati Institute of Technology & Science College in Khammam as Assistant.Professor during 2009-2011.
- ❖ Worked in Laqshya Institute of Technology & Science College from 2011-2016.
- ❖ Working in MLRITM Hyderabad from 2016-till…….

ACHIVEMENTS:

- ❖ Participated in one day work shop on VLSI technology at JNTU in Hyderabad conducted by 'AURORA ENGG. COLLEGE'.
- ❖ Undergone training in FACULTY DEVELOPMENT PROGRAMME which was conducted by Sri GAMPA NAGESWAR RAO, international personality development trainer.

PAPER PUBLICATIONNS:

- ❖ Driver assistant for drowsiness and alcohol effect detection-ISSN No: 1006-7930.- Year-2022
- ❖ HOMER-Based DES for Techno-Economic Optimization of Grid- https://link.springer.com/chapter/10.1007/978-981-33-4543-0_42.-Year-2021.

SUBJECTS HANDLED:

- ❖ STLD
- ❖ ELECTRONIC DEVICES AND CIRCUITS
- ❖ IC APPLICATIONS
- ❖ VLSI SYSTEM DESIGN

LABS HANDLED:

- ❖ PULSE AND DIGITAL CIRCUITS.
- ❖ ELECTRONIC DEVICES AND CIRCUITS.
- ❖ LINEAR INTEGRATED CIRCUIT APPLICATIONS.

PERSONAL DETAILS:

Communication address   :   H.NO:4-1-153/6, Zaheer Pura, Khammam (Dist)
Date of Birth   :   13.07.1983
Nationality   :   Indian

Declaration:

I hereby declare that the information furnished above is true to best of my knowledge.

Place: Khammam.
Date :

(D.Srinu)