

CURRICULUM VITAE

NAGARAJU KAKATI

Kakarla(village),
Julurupadu(MD),
Bhadrachalam(DT), Telangana -507166

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Objective:

- To seek a challenging position in an organization, where as a part of a team I can enhance my knowledge and utilize my skills to achieve career goals and contribute to the organization growth.

Work Experience: (9 years)

- Working as Assistant Professor in Department of ECE, **Marri Laxman Reddy Institute of Technology and Management, Dundigal**, Telangana, from March 2021 to till date **-(5 months)**.
- Worked as Assistant Professor in Department of ECE, **Khammam institute of technology and science, Khammam**, Telangana, from Dec 2017 to Feb 2021 - **(3.2years), Ratified by JNTUH.**
- Worked as Assistant Professor in Department of ECE, Adam's Engineering College Paloncha, Telangana, from **June 2016 to Dec 2017** **-(1.6 Years), Ratified by JNTUH.**
- Worked as Assistant Professor in Department of ECE, **Dr. Paul Raj Engineering College**, Bhadrachalam, from **June 2011 to June 2016** **-(5 Years)**

Educational Qualifications:

- **M.Tech** in VLSI-SD from **Sree Kavitha Engineering College** in Karepally, Khammam DT, Affiliated to JNTU Hyderabad in year **2011**, with **70.8%-First class with distinction**
- **B.Tech** in ECE **Dr. Paul Raj Engineering College**, Bhadrachalam, affiliated to JNTU-Hyderabad in year **2009**, **64.89%- First Class**
- **INTERMEDIATE** from **New Wave Junior College**, Khammam Secured **95.2%**.
- **S.S.C.** from APRS High School in ENKUR, Khammam (DT), Secured **91.5%**.

Academic Projects:

M.Tech Project Title: *FPGA IMPLEMENTATION OF LDPC DECODER FOR WI-MAX*

B.Tech Project Title: *ZIGBEE ENABLED HOME AUTOMATION SYSTEM*

Subjects Taught:

- Antenna and Wave Propagation
- VLSI Design
- Satellite communications
- Digital signal processing
- Electronic circuits
- Sensors and transducers
- Switching theory and logic design
- Digital Logic design
- Telecomm Switching Systems & NWs
- Probability theory and stochastic process
- Pulse & Digital circuits
- Analog Communication

Labs Handled:

- VLSI LAB
- Analog Communications
- Digital Communications
- Digital Signal Processing
- Linear Integrated Circuits and Applications
- Signals and Systems

Skill Set:

Programming languages : VHDL, MAT LAB, Verilog HDL.

Design tools : XILINX.

Academic Contributions:

- **Exam section In-Charge coordinator in Paul Raj Engg college during: 2009 to 2013**
- **Internal exam branch in charge in KITS, Khammam.**
- **Mini Projects Coordinator**

Administrative Skills:

- Department Placement Coordinator (2013-15)
- Department Tours In-charge, Sports In-charge, Center for Extension Activities
- Lab In-charge ECAD & VLSI.

Personal Skills:

- Good leadership qualities
- Excellent time management skills
- Efficient and effective organization skills
- Able to adapt to new situations.
- Self-motivated and dedicated to work

Research Interests:

Communication Systems, Multiple Accessing Systems

Conferences/Workshops Attended:

- Attended exam branch in charge conference at JNTUK, KAKINADA.
- Attended IEEE sponsored *international conference on RF and Signal Processing-2010* organized by the ECE Department, *K. University*.

Personal Details:

Name : **NAGARAJU K**
Sex : Male
Date of birth : 05 – 06 – 1986
Father name : Sudarshanam
Marital status: Married
Languages : English, Telugu, Hindi

Declaration:

I hereby declare that the above-mentioned information is correct up to my knowledge and I bear the responsibility for the correctness of the above-mentioned particulars.

(K Naga Raju)