

CIRRICULAM VITAE

N PALLAVI

Flat: No-208, Block-A, Kesasvini Plaza,
MNR School Road, Jyothi Nagar,
Ramachandrapuram, Hyderabad-502032

7799389141

Pallavi.goud43@gmail.com

Pallavi.nagulapally23@gmail.com

CARRER OBJECTIVES

To build the career in a growing organization, when I can get the opportunities to prove my abilities by accepting challenges, fulfilling organizational goal and climb the career ladder through continuous learning and commitment. To excel among the professionals where I can utilize my skills with strong commitment and dedication for the progressive growth of the organization.

ACADEMIC QUALIFICATIONS

- ❖ **M.TECH** in Digital Systems and Computer Organization (**DSCE**) from Marri Laxma Reddy Institute of Technology (MLRIT) 2014.
- ❖ **B.TECH** in Electronics and Communication Engineering (**ECE**) from Maheswara Institute of Technology 2011.
- ❖ **12th SSC** from Narayana Junior College 2007.
- ❖ **10th SSC** from MNR High School 2005.

PROFESSIONAL EXPERIENCE

- ❖ I have **one year teaching experience in Maheshwara engineering college**, isnapur patancheru Hyderabad from 2011-2012.
- ❖ I have **four years and six months of teaching experience in marri laxma reddy institute of technology (MLRIT)** from 2014-2019.

PAPERS PUBLISHED

- ❖ **“Implementation of digital beam former for phased array radar”**-in the international conference on electronics, communication and Aerospace technology (ICECA-2017), April 2017.At the RVS technical campus, Coimbatore, Tamilnadu, India.
- ❖ **“Design and implementation of linear frequency modulation waveform using DDS & FPGA”**-in the international conference on electronics, communication and Aerospace technology (ICECA-2017), April 2017.At the RVS technical campus, Coimbatore, Tamilnadu, India.
- ❖ **“Design and Implementation of T/R modulation with radar system through TR module controller using FPGA”**-in the international conference on electronics, communication and Aerospace technology (ICECA-2017), April 2017.At the RVS technical campus, Coimbatore, Tamilnadu, India.
- ❖ **“Design and Implementation of CMOS voltage controlled oscillator (VOC) with minimum transistors”**- at the International conference on intelligent sustainable system (ICISS-2017) organized by SCAD institute of technology on 7&8 December 2017 at palladam, Tamilnadu, India.
- ❖ **“Institute –industry and society collaboration learning bring success in engineering education”**-in International conference on recent trends in automation (ICRTA-18) held in Vasireddy venkatadri institute of technology (VVIT), Guntur, Andhra Pradesh on 15&16 Nov, 2018.
- ❖ **“Infra Red Sensor and Voice Recognition Sensor Based Vehicle Speed Control Using Embedded Controller”**-in International conference on recent trends in automation (ICRTA-18) held in Vasireddy venkatadri institute of technology (VVIT), Guntur, Andhra Pradesh on 15&16 Nov, 2018.
- ❖ **“Institute –industry and society collaboration learning bring success in engineering education”**-in International conference on recent trends in automation (ICRTA-18) held in Vasireddy venkatadri institute of technology (VVIT), Guntur, Andhra Pradesh on 15&16 Nov, 2018.

SUBJECTS AND LABS HANDLED

- ❖ STLD
- ❖ MPMC
- ❖ WCN
- ❖ AC
- ❖ DC
- ❖ ET LAB
- ❖ HDL LAB
- ❖ MPMC LAB
- ❖ ICA&HDL LAB
- ❖ BEE LAB
- ❖ ECA LAB

WORSOPS ATTENDEND

- ❖ Participated in workshop conducted by Malla Reddy Engineering College (MREC) on “communications” from 6th to 8th June, during 2015-2016.
- ❖ Participated in workshop conducted by MLR institute of technology (MLRIT) on “innovative teaching Practices and Stress Management at work place” on 30th November, during 2015-2016.
- ❖ Participated in workshop conducted by MLR institute of technology (MLRIT) on “CADENCE TOOL” from 9th to 10th may, during 2017.
- ❖ Participated in workshop conducted by MLR institute of technology (MLRIT) on “Microprocessors and Microcontrollers” from 3rd to 6th May, during 2017.
- ❖ A three day workshop by IUCEE international Engineering Pre-certification Workshop from March 8th to 10th, 2018. In MLRIT.
- ❖ Quality Improvement Program in “Instructional Design And Delivery system” Conducted by MLRLT from 2nd to 7th July 2018.

OTHER SKILLS

- ❖ Computer literate, proficiency in MS Word, PowerPoint, excels.
- ❖ Excellent language skills, basics of C and C++.
- ❖ HDL (verilog).

INTERPERSONAL SKILLS

- ❖ Highly developed interpersonal skills with an ability to work with multicultural and multidisciplinary teams.
- ❖ Good analytical and problems solving skills.
- ❖ Self-confidence and hardworking nature.
- ❖ Honesty.
- ❖ Team spirit.
- ❖ Accepting challenges.

ACADEMIC RESPONSIBILITIES

- ❖ Submitting work in a specified and agreed time.
- ❖ Accepting responsibility of our project activities.
- ❖ Being a team leader involving in analyzing the requirements and preparing documentation.

PERSONAL DETAILS

- ❖ Father's name : late N Lingamaiah
- ❖ Mother's name : N Padma
- ❖ Date of birth : 13/05/1989
- ❖ Sex : female
- ❖ Marital status : unmarried
- ❖ Languages known : Telugu, Hindi, English
- ❖ Nationality : Indian
- ❖ Hobbies : gardening, travelling, yoga practice, meditation sadana.

Declaration

I solemnly declare that all the above information is correct to the best of my knowledge and beliefs.

Date :

Place :

(N PALLAVI)