



MARRI LAXMAN REDDY
INSTITUTE OF TECHNOLOGY AND MANAGEMENT

(AN AUTONOMOUS INSTITUTION)

(Approved by AICTE, New Delhi & Affiliated to JNTUH, Hyderabad)

Accredited by NBA and NAAC with 'A' Grade & Recognized Under Section 2(f) & 12(B) of the UGC act, 1956

DEPARTMENT OF ELECTRONICS AND COMMUNICATION AND ENGINEERING

AY: 2020-2021

| S.NO | Name of the Faculty | Name of the workshops/FDPs/Seminar | Name of the Organization & Venue | FROM | TO |
|------|---------------------|------------------------------------------------------------------------------------------------------------------------------------|----------------------------------------------------------------------------------------------------------|-----------|-----------|
| 1 | Dr. SRINIVAS BACHU | Digital Design Through Verilog HDL | SVR Engineering College Nandyal | 6/14/2021 | 6/19/2021 |
| | | STTP on Embedded system design | K. C. College of Engineering and management studies and research | 6/14/2021 | 6/19/2021 |
| 2 | Dr. N. SRINIVAS | Digital Design Through Verilog HDL | SVR Engineering College Nandyal | 6/14/2021 | 6/19/2021 |
| | | STTP on Embedded system design | K. C. College of Engineering and management studies and research | 6/14/2021 | 6/19/2021 |
| | | AICTE sponsored online Short-Term Training Program (STTP) on "Machine Learning-Recent trends and Applications" (Phase-IV) | Srinivasa Ramanujan Institute of Technology (Autonomous), Ananthapuramu, Andhra Pradesh, India. | 6/21/2021 | 6/26/2021 |
| 3 | Dr. G. AMARNATH | Digital Design Through Verilog HDL | SVR Engineering College Nandyal | 6/14/2021 | 6/19/2021 |
| | | AICTE sponsored online Short-Term Training Program (STTP) on "Machine Learning-Recent trends and Applications" (Phase-IV) | Srinivasa Ramanujan Institute of Technology (Autonomous), Ananthapuramu, Andhra Pradesh, India. | 6/21/2021 | 6/26/2021 |
| | | STTP on Embedded system design | K. C. College of Engineering and management studies and research | 6/14/2021 | 6/19/2021 |
| 4 | Dr. A. Nalla Thambi | Digital Design Through Verilog HDL | SVR Engineering College Nandyal | 6/14/2021 | 6/19/2021 |

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| 5 | Dr. N.UDAYA KUMAR | Machine Learning-Recent trends and Applications | Srinivasa Ramanujan Institute of Technology (Autonomous), Ananthapuramu, Andhra Pradesh, India | 6/21/2021 | 6/26/2021 |
| | | Recent Advances in Biomedical Applications and Communication Networks | GMR Institute of technology | 7/13/2020 | 7/18/2020 |
| | | STTP on Embedded system design | K. C. College of Engineering and management studies and research | 6/14/2021 | 6/5/2021 |
| 6 | Dr.K.NAVEEN KUMAR | AICTE sponsored online Short-Term Training Program (STTP) on "Machine Learning-Recent trends and Applications" (Phase-IV) | Srinivasa Ramanujan Institute of Technology (Autonomous), Ananthapuramu, Andhra Pradesh, India. | 6/21/2021 | 6/26/2021 |
| 7 | Dr. SHIJIN KUMAR P.S | Embedded Systems Using 8051 Micro Controller | NITTTR Chennai | 6/21/2021 | 6/25/2021 |
| 8 | Dr. KISHORE SANAPALA | Digital Design Through Verilog HDL | SVR Engineering College Nandyal | 6/14/2021 | 6/19/2021 |
| | | System Engineering | Maulana Azad National Institute Of Technology Bhopal | 6/21/2021 | 6/25/2021 |
| 9 | S.ARAVIND KUMAR | Digital Design Through Verilog HDL | SVR Engineering College Nandyal | 6/14/2021 | 6/19/2021 |
| 10 | KUNDETI BUSHAN | Digital Design Through Verilog HDL | SVR Engineering College Nandyal | 6/14/2021 | 6/19/2021 |
| | | 5-Day Workshop on VLSI Device and Circuit Design Tools | School of Electronics Engineering (SENSE) in association with Academic Staff College, VIT-AP University, Near Vijayawada (AP) | 6/22/2021 | 6/26/2021 |
| 11 | I.ADUMBABU | Digital Design Through Verilog HDL | SVR Engineering College Nandyal | 6/14/2021 | 6/19/2021 |

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| 12 | B.N.SRINIVAS | 5- Day Workshop on Technical Report Writing using LaTeX” | School of Electronics Engineering (SENSE), VIT-AP University, Amaravati (AP), India | 4/5/2021 | 4/9/2021 |
| | | STTP on Embedded system design | K. C. College of Engineering and management studies and research | 6/14/2021 | 6/19/2021 |
| | | Digital Design Through Verilog HDL | SVR Engineering College Nandyal | 6/14/2021 | 6/19/2021 |
| | | 5G and Emerging technologies | CVR college of Engineering | 8/9/2021 | 8/14/2021 |
| 13 | B.KOTESWAR RAO | Digital Design Through Verilog HDL | SVR Engineering College Nandyal | 6/14/2021 | 6/19/2021 |
| | | STTP on Embedded system design | K. C. College of Engineering and management studies and research | 6/14/2021 | 6/19/2021 |
| 14 | J.NARANDAR | Embedded Systems Using 8051 Micro Controller | NITTTR Chennai | 6/21/2021 | 6/25/2021 |
| 15 | E SREENIVASULU | Digital Design Through Verilog HDL | SVR Engineering College Nandyal | 6/14/2021 | 6/19/2021 |
| 16 | Mrs. K. S. MONICA | Embedded Systems Using 8051 Micro Controller | NITTTR Chennai | 6/21/2021 | 6/25/2021 |
| | | Digital Design Through Verilog HDL | SVR Engineering College Nandyal | 6/14/2021 | 6/19/2021 |
| | | Machine Learning-Recent trends and Applications | Srinivasa Ramanujan Institute of Technology (Autonomous), Ananthapuramu, Andhra Pradesh, India | 6/21/2021 | 6/26/2021 |
| 17 | Mr.RUPAKUMAR DHANAVATH | Digital Design Through Verilog HDL | SVR Engineering College Nandyal | 6/14/2021 | 6/19/2021 |
| | | STTP on Embedded system design | K. C. College of Engineering and management studies and research | 6/14/2021 | 6/19/2021 |

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| 18 | LINGALA NARESH KUMAR | Digital Design Through Verilog HDL | SVR Engineering College Nandyal | 6/14/2021 | 6/19/2021 |
| 19 | DHONVAN SRINU | Embedded Systems Using 8051 Micro Controller | NITTTR Chennai | 6/21/2021 | 6/25/2021 |
| | | Digital Design Through Verilog HDL | SVR Engineering College Nandyal | 6/14/2021 | 6/19/2021 |
| 20 | SINDHU REHKA | 5-Day Workshop on VLSI Device and Circuit Design Tools | School of Electronics Engineering (SENSE) in association with Academic Staff College, VIT-AP University, Near Vijayawada (AP) | 6/22/2021 | 6/26/2021 |
| | | Embedded Systems Using 8051 Micro Controller | NITTTR Chennai | 6/21/2021 | 6/25/2021 |
| 21 | K.PRATAP KHANA | Digital Design Through Verilog HDL | SVR Engineering College Nandyal | 6/14/2021 | 6/19/2021 |
| 22 | S.UMA RANI | Digital Design Through Verilog HDL | SVR Engineering College Nandyal | 6/14/2021 | 6/19/2021 |
| 23 | G.KIRAN KUMAR | Digital Design Through Verilog HDL | SVR Engineering College Nandyal | 6/14/2021 | 6/19/2021 |
| 24 | SURESH KUMAR K V | MLDL By Using COLAB And KNIME - A Road Map | CMREC | 6/1/2021 | 6/5/2021 |
| | | STTP on Embedded system design | K. C. College of Engineering and management studies and research | 6/14/2021 | 6/19/2021 |
| | | Digital Design Through Verilog HDL | SVR Engineering College Nandyal | 6/14/2021 | 6/19/2021 |
| 25 | P.SANDHYA | Embedded Systems Using 8051 Micro Controller | NITTTR Chennai | 6/21/2021 | 6/25/2021 |
| | | Digital Design Through Verilog HDL | SVR Engineering College Nandyal | 6/14/2021 | 6/19/2021 |

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| | | STTP on Embedded system design | K. C. College of Engineering and management studies and research | 6/14/2021 | 6/19/2021 |
| 26 | S.K.HIMA BINDHU | Embedded Systems Using 8051 Micro Controller | NITTTR Chennai | 6/21/2021 | 6/25/2021 |
| | | Digital Design Through Verilog HDL | SVR Engineering College Nandyal | 6/14/2021 | 6/19/2021 |
| | | STTP on Embedded system design | K. C. College of Engineering and management studies and research | 6/14/2021 | 6/19/2021 |
| 27 | KANAKALA KAVITHA | AICTE sponsored online Short-Term Training Program (STTP) on "Machine Learning-Recent trends and Applications" (Phase-IV) | Srinivasa Ramanujan Institute of Technology (Autonomous), Ananthapuramu, Andhra Pradesh, India. | 6/21/2021 | 6/26/2021 |
| 28 | V.KOTESWARA RAO | Digital Design Through Verilog HDL | SVR Engineering College Nandyal | 6/14/2021 | 6/19/2021 |
| | | Embedded Systems Using 8051 Micro Controller | NITTTR Chennai | 6/21/2021 | 6/25/2021 |
| | | STTP on Embedded system design | K. C. College of Engineering and management studies and research | 6/14/2021 | 6/19/2021 |
| 29 | T.TANUJA | Embedded Systems using 8051 Micro Controller | NITTTR Chennai | 21.06.2021 | 6/25/2021 |
| 30 | V RAKESH KUMAR | Embedded Systems Using 8051 Micro Controller | NITTTR Chennai | 6/21/2021 | 6/25/2021 |
| 31 | IMMANUEL | Embedded Systems Using 8051 Micro Controller | NITTTR Chennai | 6/21/2021 | 6/25/2021 |
| 32 | Y.SATYANARAYANAM MA | AICTE sponsored online Short-Term Training Program (STTP) on "Machine Learning-Recent trends and Applications" (Phase-IV) | Srinivasa Ramanujan Institute of Technology (Autonomous), Ananthapuramu, Andhra Pradesh, India. | 6/21/2021 | 6/26/2021 |
| | | Digital Design Through Verilog HDL | SVR Engineering College Nandyal | 6/14/2021 | 6/19/2021 |

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| 33 | RESHMA BINDU NIMMAGADDA | STTP on Embedded system design | K. C. College of Engineering and management studies and research | 6/14/2021 | 6/19/2021 |
| | | Embedded Systems using 8051 Micro Controller | NITTTR Chennai | 6/21/2021 | 6/25/2021 |
| 34 | MALATHI RANI | Embedded Systems using 8051 Micro Controller | NITTTR Chennai | 21.06.2021 | 6/25/2021 |
| | | Digital Design Through Verilog HDL | SVR Engineering College Nandyal | 6/14/2021 | 6/19/2021 |
| | | Machine Learning-Recent trends and Applications | Srinivasa Ramanujan Institute of Technology (Autonomous), Ananthapuramu, Andhra Pradesh, India | 6/21/2021 | 6/26/2021 |
| 35 | CHANDRIKA SAXENA | STTP on Embedded system design | K. C. College of Engineering and management studies and research | 6/14/2021 | 6/19/2021 |
| | | AICTE sponsored online Short-Term Training Program (STTP) on "Machine Learning-Recent trends and Applications" (Phase-IV) | Srinivasa Ramanujan Institute of Technology (Autonomous), Ananthapuramu, Andhra Pradesh, India. | 6/21/2021 | 6/26/2021 |
| 36 | THOTAPALLI VINAYAKUMAR | Digital Design Through Verilog HDL | SVR Engineering College Nandyal | 6/14/2021 | 6/19/2021 |
| | | STTP on Embedded system design | K. C. College of Engineering and management studies and research | 6/14/2021 | 6/19/2021 |
| | | STTP on Embedded system design | K. C. College of Engineering and management studies and research | 6/14/2021 | 6/19/2021 |

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| 37 | D JAYAKUAMR | Machine Learning-Recent trends and Applications | Srinivasa Ramanujan Institute of Technology (Autonomous), Ananthapuramu, Andhra Pradesh, India | 6/21/2021 | 6/26/2021 |
| 38 | B.BALAJI | Digital Design Through Verilog HDL | SVR Engineering College Nandyal | 6/14/2021 | 6/19/2021 |
| | | STTP on Embedded system design | K. C. College of Engineering and management studies and research | 6/14/2021 | 6/19/2021 |
| | | Machine Learning-Recent trends and Applications | Srinivasa Ramanujan Institute of Technology (Autonomous), Ananthapuramu, Andhra Pradesh, India | 6/21/2021 | 6/26/2021 |
| 39 | N.PALLAVI | STTP on Embedded system design | K. C. College of Engineering and management studies and research | 6/14/2021 | 6/19/2021 |
| | | Digital Design Through Verilog HDL | SVR Engineering College Nandyal | 6/14/2021 | 6/19/2021 |
| 40 | K.NAGARAJU | Energy and Environmental Science,Technology and Sustainable Management | Dr.D.Y. Patil Institute Of Technology, Pune | 5/20/2021 | 5/25/2021 |
| | | MLDL By Using COLAB And KNIME - A Road Map | CMREC | 6/1/2021 | 6/5/2021 |

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| 41 | K.NAGAMANI | Digital Design Through Verilog HDL | SVR Engineering College Nandyal | 6/14/2021 | 6/19/2021 |
| | | STTP on Embedded system design | K. C. College of Engineering and management studies and research | 6/14/2021 | 6/19/2021 |
| | | Embedded Systems using 8051 Micro Controller | NITTTR Chennai | 6/21/2021 | 6/25/2021 |
| 42 | M.SUPRIYA | Digital Design Through Verilog HDL | SVR Engineering College Nandyal | 6/14/2021 | 6/19/2021 |
| | | Embedded System design | K. C. College of Engineering and management studies and research | 6/14/2021 | 6/19/2021 |
| | | Embedded Systems using 8051 Micro Controller | NITTTR Chennai | 6/21/2021 | 6/25/2021 |
| 43 | H.SANGEETHA | Digital Design Through Verilog HDL | SVR Engineering College Nandyal | 6/14/2021 | 6/19/2021 |
| | | STTP on Embedded system design | K. C. College of Engineering and management studies and research | 6/14/2021 | 6/19/2021 |
| 44 | P.KAVERI | Embedded Systems using 8051 Micro Controller | NITTTR Chennai | 6/21/2021 | 6/25/2021 |
| 45 | K.DIVYA | STTP on Embedded system design | K. C. College of Engineering and management studies and research | 6/14/2021 | 6/19/2021 |
| | | Digital Design Through Verilog HDL | SVR Engineering College Nandyal | 6/14/2021 | 6/19/2021 |
| 46 | K.VIJAY KUMAR | Digital Design Through Verilog HDL | SVR Engineering College Nandyal | 6/14/2021 | 6/19/2021 |
| 47 | K.GANESH | Digital Design Through Verilog HDL | SVR Engineering College Nandyal | 6/14/2021 | 6/19/2021 |
| 48 | A Anil Kumar | Embedded Systems using 8051 Micro Controller | NITTTR Chennai | 6/21/2021 | 6/25/2021 |
| 49 | K Santhoshi | Digital Design Through Verilog HDL | SVR Engineering College Nandyal | 6/14/2021 | 6/19/2021 |
| 50 | V Chandana | Embedded Systems using 8051 Micro Controller | NITTTR Chennai | 6/21/2021 | 6/25/2021 |